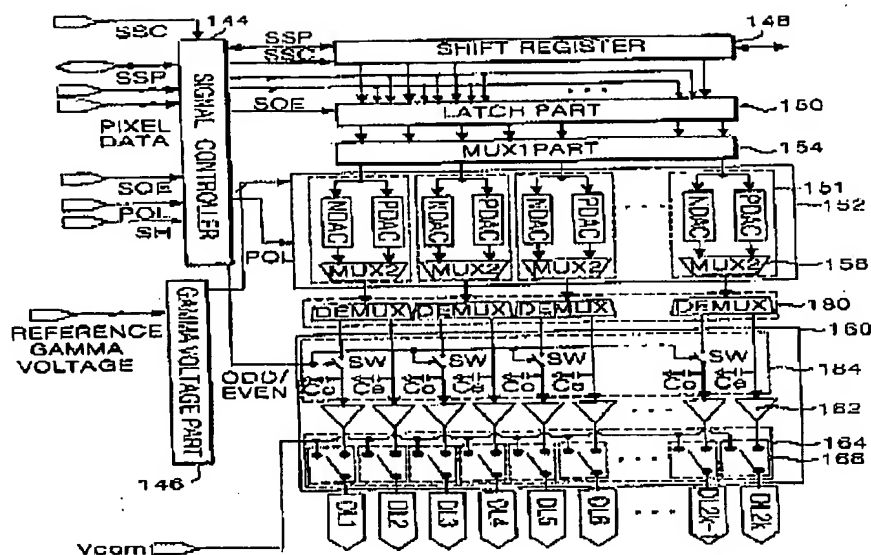
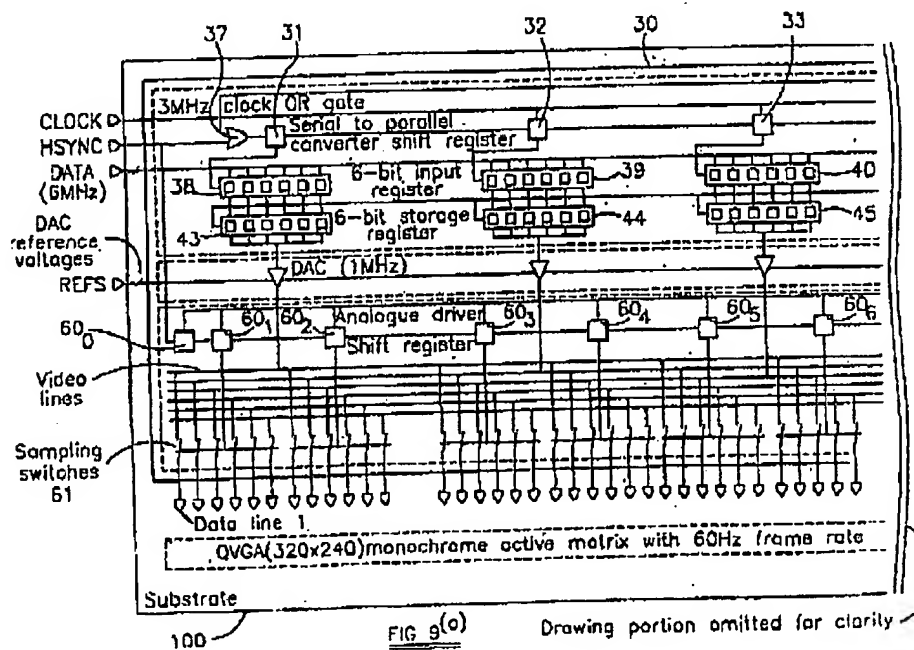


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FIG. 5



At page 6 of the Office Action, the Examiner cites paragraph [0061] of Cairns1 as teaching the aforementioned features of claim 9. However, Applicant submits Cairns1 fails to disclose the aforementioned features of claim 9. As shown below, Figure 9(b) of Cairns1, Cairns1 does not show that Vcom voltage is supplied to output portion between an m phase analog driver 22 and matrix 1.



With respect to paragraph [0061], Cairns1 discloses that DACs 21 receive the reference voltage from common reference voltage bus. However, the reference voltage of Cairns1 corresponds to the reference gamma voltage in Figure 5 of the present application, which is

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different from the Vcom voltage of the claimed invention. In other words, the Vcom voltage is a separate, recited element and different from the reference gamma voltage as discussed above.

Accordingly, Applicants respectfully submit that claim 9 and claims 11, 12, 15-27 and 29, which depend from claim 9, are allowable over the cited references.

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